

Feature	Technical Explanation	Benefits
New APG Functions	<ul style="list-style-type: none"> • Clustertest • Generation of VB. NET-Code • Adjustable text length • Better version handling 	<ul style="list-style-type: none"> • Automatically higher test coverage • Free choice of programming language • Avoidance of long texts • Weighting according to component relevance • Avoid redundant tests by combining identical tests
DebugRobot (optional)	<ul style="list-style-type: none"> • Fully automated debugging • Transistor testing made easy • Diodes can be learned automatically 	<ul style="list-style-type: none"> • Automatic debugging, fully automated • The potential for errors is reduced • Saving costs and time • Faster time to market for new products (NPI)
Adjusting Layout Display	<ul style="list-style-type: none"> • Communication with Kernel → layout selectable 	<ul style="list-style-type: none"> • Avoiding error messages due to non-conforming error texts
Retest Failing Components	<ul style="list-style-type: none"> • Simple and reliable repetition of failed tests 	<ul style="list-style-type: none"> • Significant time savings with board retests
Free Access To Test Results	<ul style="list-style-type: none"> • Extended callback functions for dynamic collection of test data 	<ul style="list-style-type: none"> • Freely configurable test program information transfer without file transfer or direct creation of your own file format
Visual Basic Support	<ul style="list-style-type: none"> • Support for all current VB versions 	<ul style="list-style-type: none"> • Free choice of existing company licenses
Test Programm Batch Translator	<ul style="list-style-type: none"> • Automatically translate & release all existing test programmes 	<ul style="list-style-type: none"> • Serious time savings with CITE updates
FailSim Advantages	<ul style="list-style-type: none"> • Simplified operation when debugging • Larger test scope 	<ul style="list-style-type: none"> • Save time during debugging • Verification of further test methods (e.g.parallel combinations)
USM-Module & AMU-SPS Port CITE Integration	<ul style="list-style-type: none"> • Corresponding kernel commands implemented 	<ul style="list-style-type: none"> • Simple programming without low level procedures
Extension Of Test Log File	<ul style="list-style-type: none"> • All relevant test parameters in the XML log file 	<ul style="list-style-type: none"> • Standardized XML format • Compatible expandable at any time • Access to all test information for any evaluations
CITE With Reduced User Rights (optional)	<ul style="list-style-type: none"> • Secure, concealed access to Windows admin rights 	<ul style="list-style-type: none"> • Manage User Rights easily
Standardized Remote Control App	<ul style="list-style-type: none"> • Remote control of CITE from superior instance 	<ul style="list-style-type: none"> • Clearly stuctured processes such as Process locking • Realization of Industry 4.0 algorithms